

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA Facsimile Transmission to: COMMISSIONER OF PATENTS AND TRADEMARKS, Alexandria VA. 22313, on May 5, 2005 by: Teri McDonald

Teri McDonald 5/6/05
Signature & Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: David J. Hathaway, et al.	Date: May 5, 2005
Serial Number: 10/078,876	Examiner: Joseph D. Torres
Filed: 02/20/2002	Group Art Unit: 2133
Title: Method for Reducing Switching Activity during a Scan Operation with Limited Impact on the Test Coverage of an Integrated Circuit	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

RESPONSE TO FINAL OFFICE ACTION AND TO ADVISORY ACTION

The Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Mail Stop: AF

Sir:

Responsive to the Advisory Action dated April 19, 2005, the following amendment is being submitted to place the above identified application in prima facie condition for allowance.

Please enter the Claims and consider the Remarks as follows:

10/078,876

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ok to enter
5/16/05
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